

# Reliability Evaluation of Power Electronic Inverter Using Cutset Approach

Palthur Shashavali<sup>1\*</sup>, V. Sankar<sup>2</sup>

<sup>1</sup> Dept. of EEE,

S.K.U College of Engineering & Technology, Ananthapuramu - 515 003, INDIA

<sup>2</sup> Department of EEE,

JNTUACEA (A), Ananthapuramu - 515 002, INDIA

\*Corresponding Author: [shashavali240@gmail.com](mailto:shashavali240@gmail.com)

DOI: <https://doi.org/10.30880/ijie.2024.16.03.027>

## Article Info

Received: 6 February 2024

Accepted: 14 November 2024

Available online: 23 November 2024

## Keywords

Basic probability indices (bpi), probabilistic measures (pm), cutset, failure rate (fr), probabilistic logic diagram (pld)

## Abstract

The development of PV systems for generation of renewable energy has been gaining lot of importance in the present day research. Moreover, probabilistic analysis of the various configuration of PV cell, Power Electronic converters etc. has been considered as the potential area of research with the appropriate assumptions. Evaluation of Basic Probability Indices (BPI) and Probabilistic Measures (PM) of an inverter configuration has been dealt with in the literature by considering failure of all components will lead to inverter failure, treating all such components in series from Probabilistic Logic Diagram (PLD). However, the evaluation of BPI has not been dealt with considering the operating strategies of the inverter.

In this paper, it is proposed to deal with the evaluation of the BPIs and PMs for single phase full bridge inverter with R and R-L Load conditions and the results of the proposed methods using cutsets will be compared with an existing method.

## 1. Introduction

Reliability evaluation of converter system used in solar PV systems has been gaining importance in the literature. Basic probability methods used in systems approach of network configurations has been dealt with in [4, 11]. The probabilistic methods of power system and HVDC reliability evaluations are given in [5]. A cutset approach for reliability evaluations of HVDC systems has been developed in which the concept of flow networks has been used and the probability of failure of transmission of required flow at the D.C terminal has been obtained [2]. Probabilistic failure data of the Semi-Conductor devices like diode, SCR, IGBT and components such as capacitor, inductor etc., are given in [1, 7]. Reliability evaluation and schematic diagrams of power system using PV arrays using inverters has been given in [9, 16-28].

A probabilistic method using minimal paths for multi-level converters has been described in [8]. More electric aircraft power system has been discussed in [13]. A dynamic Bayesian network approach for PV systems considering intermittent faults has been presented in [12]. Life time and array sizing of inverter with PV system has been presented in [14] using Monte Carlo Simulation technique. The reliability evaluations of PV array with different shaded configurations has been presented and a comparison is made [15].

Although different methods have been developed as stated above, cutset approach for reliability evaluations has not been discussed in detail. In [8], minimal path based approach is presented which is a combinational problem. The advantages of making reliability evaluation using cutset approach are well known in the literature [2, 4, 11].

In the literature, the authors have computed basic Probability indices [4, 11]. For single phase inverter configuration, all the elements are assumed to be in series from the Probability Logic Diagram point of view as the authors have considered that all the components are required for success state of the Inverter [9]. In this context, it is thought that cutsets from the Inverter configuration can be taken based on the performance of the inverter output results for different loading conditions.

In this paper, it is proposed to compute BPIs and PMs of single phase full bridge inverter configuration using cutset based approach. Computational results will be presented based on the failure rate of components such as SCRs, diodes, capacitor etc. Further, the comparison of Reliability Indices with the existing method [9] with the proposed method will be presented and analysed.

## 2. Proposed Methodology

In this section, computation of basic failure rates of components in the inverter and procedure for evaluation of Reliability Indices are presented.

### 2.1 Computation of Failure Rates for Components

In this paper, computation of failure rates of components of the single phase bridge inverter configuration as shown in Fig. 1 is considered.

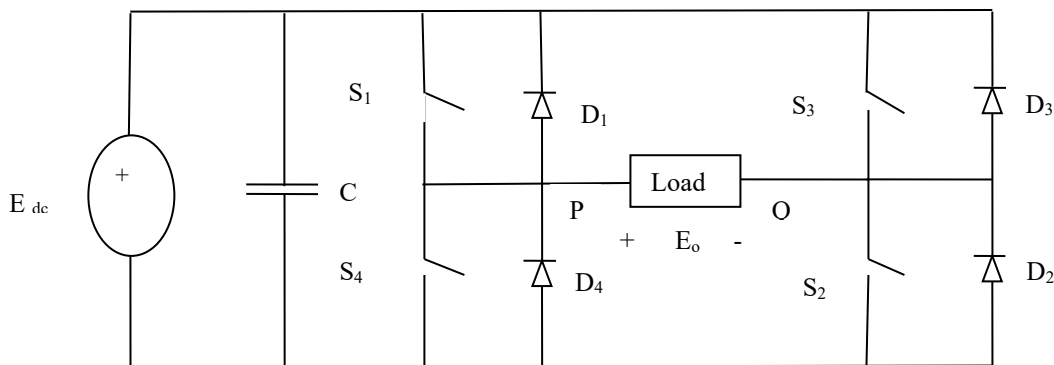


Fig. 1 Single phase full bridge inverter

The failure rates of components number of Failures per Million hours (F/Mhr) can be estimated as follows [9]:

- i) For SCR;  $\lambda_{ij}$  is the element in inverter  $i, j = \text{SCR, Diode}$ .

$$\lambda_{\text{SCR}} = \lambda_{\text{Sb}} \Pi_{\text{Ts}} \Pi_{\text{Qs}} \Pi_{\text{Es}} \Pi_{\text{Vs}} \text{ F / Mhr} \tag{1}$$

- ii) For Diodes

$$\lambda_{\text{D}} = \lambda_{\text{db}} \Pi_{\text{Td}} \Pi_{\text{Sd}} \Pi_{\text{Cd}} \Pi_{\text{Qd}} \Pi_{\text{Ed}} \text{ F / Mhr} \tag{2}$$

- iii) For the Capacitor

$$\lambda_{\text{C}} = \lambda_{\text{cb}} \Pi_{\text{Vc}} \Pi_{\text{CSR}} \Pi_{\text{Qc}} \Pi_{\text{Ec}} \text{ F / Mhr} \tag{3}$$

where  $\lambda_{\text{Sb}}, \lambda_{\text{db}}$  and  $\lambda_{\text{cb}}$  are the base failure rate of the devices SCR, and Diode respectively.

- $\Pi_{\text{Ts}}, \Pi_{\text{Td}}$ , are the Temperature factors of the devices SCR, Diode, and Capacitor respectively.
- $\Pi_{\text{Qs}}, \Pi_{\text{Qd}}, \Pi_{\text{Qc}}$  are the Quality factors of the devices SCR, Diode, and Capacitor respectively.
- $\Pi_{\text{Es}}, \Pi_{\text{Ed}}, \Pi_{\text{Ec}}$  are the Environmental factor of the devices SCR, Diode, and Capacitor respectively.
- $\Pi_{\text{Vc}}$  is the Capacitance factor for the capacitor.
- $\Pi_{\text{CSR}}$  is the series resistance factor for the capacitor.

### 2.2 Modelling of the Proposed Method

As stated in the Section 2.1, cutset approach of evaluation of basic probabilities indices has more significant advantages as cutsets directly represent the failure modes of any configuration. Consider the single phase full

bridge inverter (voltage source inverter) Configuration with SCR, diodes, capacitor and voltage source as shown in Fig. 1.

The cutset of the configurations can be obtained with failures of

- i) Only SCRs with R-Load
- ii) Combinations of SCRs and Diode failures with R-L Load.
- iii) Malfunctioning of the one of the OFF-state SCRs while two SCRs are in one of the operating modes.

**Assumptions:**

- 1) It is assumed the probability of the voltage source is unity i.e. No failure of the voltage source is considered.
- 2) Further, it is also assumed that in practical inverter, failures of the only diodes, which can be considered as cutset, have been neglected as they will not affect the inverter operations in general.
- 3) Moreover, failure of the capacitor has been considered in the analysis.
- 4) The repair time (in hrs.) for all the components, i.e. SCR, Diode, Capacitors is assumed to be same.

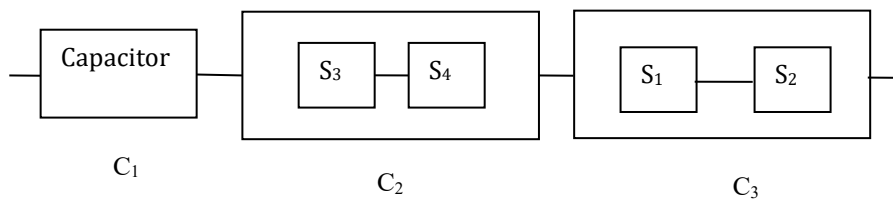
Based on the above discussion, the PLD using the cutset models have been developed. The formula for computations of basic probability indices such as Equivalent Failure Rate (EFR)  $\lambda_{eq}$ , Mean Outage Time (MOT)  $r_{eq}$ , Average Annual Outage Time (AAOT)  $U_{eq}$  and PMs such as Mean Time To Failure (MTTF), Mean Time To Repair (MTTR), Mean Time Between Failure (MTBF), Availability and Unavailability have been presented as follows [10, 11].

**2.3 Evaluation of BPI and PM for Inverter with R-Load**

Probability Logic Diagram for an Inverter using cutset with failures of SCRs with R-Load. Based on the inverter operation [6] the following three cutsets have been obtained (for Fig.1)

C<sub>1</sub>: Capacitor      C<sub>2</sub>: S<sub>1</sub> S<sub>2</sub>      C<sub>3</sub>: S<sub>3</sub> S<sub>4</sub>

The corresponding PLD and the corresponding simplifications are shown in Figs. 2 to 4 respectively.



**Fig. 2** Probability logic diagram of a single phase inverter with R-Load

The expressions for basic probability indices for Fig.2 can be written as [4, 11]

For Cutset 1:

$$\lambda_{C_1} = \lambda_{\text{Capacitor}} \tag{4}$$

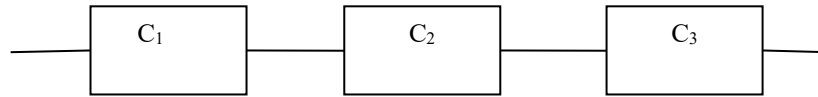
For Cutset 2:

$$\lambda_{C_2} = \lambda_{S_3} + \lambda_{S_4} \tag{5}$$

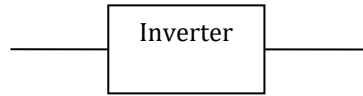
For Cutset 3:

$$\lambda_{C_3} = \lambda_{S_1} + \lambda_{S_2} \tag{6}$$

Where  $\lambda_{S_1}, \lambda_{S_2}, \lambda_{S_3}, \lambda_{S_4}$  are the failure rates of the SCRs which can be obtained from equations given in Section 2.1. Fig. 2 is reduced to Fig. 3 and Fig. 4 using network reduction technique.



**Fig. 3** Reduced PLD of a single phase inverter for R-Load



**Fig. 4** Final equivalent for R-Load

EFR for Inverter:

$$\lambda_1 = \lambda_{C_1} + \lambda_{C_2} + \lambda_{C_3} \tag{7}$$

Average Annual Outage Time of the inverter  $U_1$  from Fig.3 can be expressed as:

$$AAOT = U_1 = \lambda_{C_1} I_{C_1} + \lambda_{C_2} I_{C_2} + \lambda_{C_3} I_{C_3} \tag{8}$$

Where  $I_{C_2} = \frac{\lambda_{S_3} r_{S_3} + \lambda_{S_4} r_{S_4}}{\lambda_{S_3} + \lambda_{S_4}} ; I_{C_3} = \frac{\lambda_{S_1} r_{S_1} + \lambda_{S_2} r_{S_2}}{\lambda_{S_1} + \lambda_{S_2}}$  (9)

$$MOT_{of\ Inverter} = r_1 = \frac{U_1}{\lambda_1} \tag{10}$$

The PMs of Inverter with R-Load will be obtained from Eqns. (5.11) to (5.15) and is as follows:

$$MTTF_1 = \frac{1}{\lambda_1} \tag{11}$$

$$MTTR_1 = r_1 \tag{12}$$

$$MTBF_1 = MTTF_1 + MTTR_1 \tag{13}$$

$$A_1 = \frac{MTTR_1}{MTBF_1} \tag{14}$$

$$U_1 = (1 - A_1) \tag{15}$$

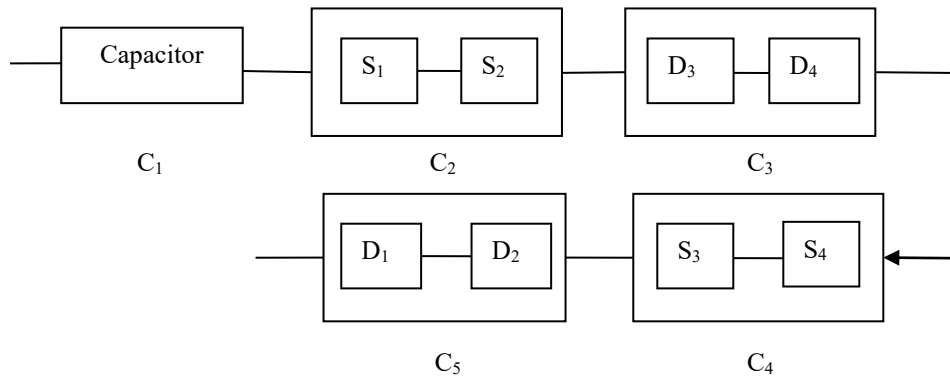
## 2.4 Evaluation of BPI and PM for Inverter with R-L Load

In this section modelling of the PLDs has been developed for configurations (ii) and (iii) as stated in section 2.2 and equations for computation of reliability indices are presented.

### 2.4.1 Without Malfunctioning

Based on the inverter operation [6] the following five cutsets have been obtained (for Fig.1)

- C1: Capacitor
- C2: S<sub>1</sub> S<sub>2</sub>
- C3: D<sub>3</sub> D<sub>4</sub>
- C4: S<sub>3</sub> S<sub>4</sub>
- C5: D<sub>1</sub> D<sub>2</sub>



**Fig. 5** PLD of a single phase inverter with R-L-Load

For Cutset 1: 
$$\lambda_{C_1} = \lambda_{\text{Capacitor}} \tag{16}$$

For Cutset 2: 
$$\lambda_{C_2} = \lambda_{S_1} + \lambda_{S_2} \tag{17}$$

$$U_{C_2} = \lambda_{S_1} r_{S_1} + \lambda_{S_2} r_{S_2} \tag{18}$$

$$r_{C_2} = \frac{U_{C_2}}{\lambda_{C_2}} \tag{19}$$

For Cutset 3: 
$$\lambda_{C_3} = \lambda_{D_3} + \lambda_{D_4} \tag{20}$$

$$U_{C_3} = \lambda_{D_3} r_{D_3} + \lambda_{D_4} r_{D_4} \tag{21}$$

$$r_{C_3} = \frac{U_{C_3}}{\lambda_{C_3}} \tag{22}$$

For Cutset 4: 
$$\lambda_{C_4} = \lambda_{S_3} + \lambda_{S_4} \tag{23}$$

$$U_{C_4} = \lambda_{S_3} r_{S_3} + \lambda_{S_4} r_{S_4} \tag{24}$$

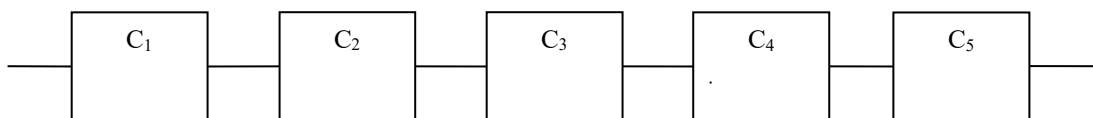
$$r_{C_4} = \frac{U_{C_4}}{\lambda_{C_4}} \tag{25}$$

For Cutset 5: 
$$\lambda_{C_5} = \lambda_{D_1} + \lambda_{D_2} \tag{26}$$

$$U_{C_5} = \lambda_{D_1} r_{D_1} + \lambda_{D_2} r_{D_2} \tag{27}$$

$$r_{C_5} = \frac{U_{C_5}}{\lambda_{C_5}} \tag{28}$$

Fig. 5 is reduced to Figs. 6 and 7 using network reduction technique. Accordingly, the simplification of PLD can be written as:



**Fig. 6** Final reduction of probability logic diagram with R-L load



**Fig. 7** Final equivalent of PLD for R-L Load

The evaluation of basic Probability indices for the single phase Full Bridge inverter with R-L Load with NMF will be as follows:

$$EFR = \lambda_{I-NMF} = \lambda_{C_1} + \lambda_{C_2} + \lambda_{C_3} + \lambda_{C_4} + \lambda_{C_5} \tag{29}$$

$$AAOT = U_{I-NMF} = \lambda_{C_1} r_{C_1} + \lambda_{C_2} r_{C_2} + \lambda_{C_3} r_{C_3} + \lambda_{C_4} r_{C_4} + \lambda_{C_5} r_{C_5} \tag{30}$$

$$MOT = r_{I-NMF} = \frac{U_{I-NMF}}{\lambda_{I-NMF}} \tag{31}$$

The PMs of Inverter with R-L Load and with NMF can be obtained similarly from Eqns. (11) to (15) respectively.

### 2.4.2 With Malfunctioning

When two of the SCRs are conducting in operating mode, any one of the remaining two SCRs which are expected to be in the OFF-State, if it were to be in ON-State then there will be a fatal failure of inverter. With this aspect, the modelling of PLDs with cutsets has been also dealt with (referred to as malfunctioning). Based on the inverter operation [6] with malfunctioning, the following four Cutsets have been obtained (for Fig.1)

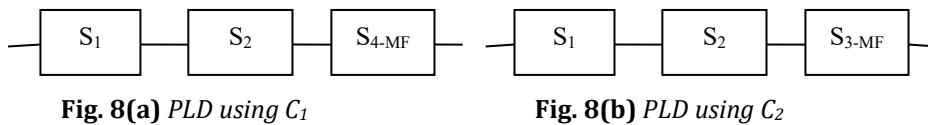
**C<sub>1</sub>**: S<sub>1</sub>, S<sub>2</sub> ON and S<sub>4</sub> is malfunctioning it leading to failure of inverter, denoted as S<sub>4-MF</sub>.

**C<sub>2</sub>**: S<sub>1</sub>, S<sub>2</sub> ON and S<sub>3</sub> is malfunctioning it leading to failure of inverter, denoted as S<sub>3-MF</sub>.

**C<sub>3</sub>**: S<sub>3</sub>, S<sub>4</sub> ON and S<sub>1</sub> is malfunctioning it leading to failure of inverter, denoted as S<sub>1-MF</sub>.

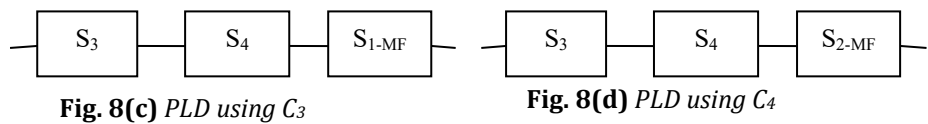
**C<sub>4</sub>**: S<sub>3</sub>, S<sub>4</sub> ON and S<sub>2</sub> is malfunctioning it leading to failure of inverter, denoted as S<sub>2-MF</sub>.

Although C<sub>1</sub> consists of three elements with two of the SCRs functioning and one SCR is malfunctioning, simultaneously all the three elements in ON-State will lead to the fatal failure of the inverter. Hence, all the elements of C<sub>1</sub> are considered to be in series as shown in Fig.8 (a). Similarly for other cutsets C<sub>2</sub>, C<sub>3</sub> and C<sub>4</sub>, which are shown in Fig. 8(b) to 8(d).



**Fig. 8(a)** PLD using C<sub>1</sub>

**Fig. 8(b)** PLD using C<sub>2</sub>



**Fig. 8(c)** PLD using C<sub>3</sub>

**Fig. 8(d)** PLD using C<sub>4</sub>

The corresponding developed equations from Fig. 8(a) will be based on Union Rule of the unreliability for series configuration [4, 11].

$$Q_{C_1} = Q_{S_1} + Q_{S_2} + Q_{S_{4-MF}} - Q_{S_1} Q_{S_2} - Q_{S_1} Q_{S_{4-MF}} - Q_{S_2} Q_{S_{4-MF}} + Q_{S_1} Q_{S_2} Q_{S_{4-MF}} \tag{32}$$

In the Eqn. (27), neglecting higher order terms, which can be referred to as summation rule [4, 11], expressed as:

$$Q_{C_1} = Q_{S_1} + Q_{S_2} + Q_{S_{4-MF}} \tag{33}$$

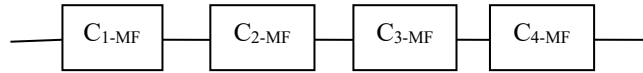
Similarly, from Figs. 8(b) to 8(d).

$$Q_{C_2} = Q_{S_1} + Q_{S_2} + Q_{S_3-MF} \tag{34}$$

$$Q_{C_3} = Q_{S_3} + Q_{S_4} + Q_{S_1-MF} \tag{35}$$

$$Q_{C_4} = Q_{S_3} + Q_{S_4} + Q_{S_2-MF} \tag{36}$$

And the corresponding reductions to single equivalent element for the inverter with malfunctioning R-L Load is as shown in Fig. 8(e) and Fig. 8(f) respectively.



**Fig. 8(e)** PLD of inverter for R-L load considering



**Fig. 8(f)** Final equivalent of PLD with MF

The unreliability of the inverter from Fig. 8(e) can be obtained as:

$$Q_{I-MF} = Q_{C_1} + Q_{C_2} + Q_{C_3} + Q_{C_4} \tag{37}$$

The reliability of the inverter from Fig. 8(e).

$$R_{I-MF} = (1 - Q_{I-MF}) \tag{38}$$

$$R_{I-MF} = \left( \frac{\mu_{I-MF}}{\lambda_{I-MF} + \mu_{I-MF}} \right) \tag{39}$$

Where  $\mu_{im}$  be the transitional rate of the inverter during malfunctioning. From eqn. (39), Compute  $\mu_{I-MF}$ . From Fig.8 (a) to Fig. 8(d).

$$\lambda_{C_1-MF} = \lambda_{S_1} + \lambda_{S_2} + \lambda_{S_4-MF} \tag{40}$$

$$\lambda_{C_2-MF} = \lambda_{S_1} + \lambda_{S_2} + \lambda_{S_3-MF} \tag{41}$$

$$\lambda_{C_3-MF} = \lambda_{S_3} + \lambda_{S_4} + \lambda_{S_1-MF} \tag{42}$$

$$\lambda_{C_4-MF} = \lambda_{S_3} + \lambda_{S_4} + \lambda_{S_2-MF} \tag{43}$$

The BPIs of inverter with malfunctioning is as follows:

$$\lambda_{I-MF} = \lambda_{C_1-MF} + \lambda_{C_2-MF} + \lambda_{C_3-MF} + \lambda_{C_4-MF} \tag{44}$$

$$I_{I-MF} = \frac{1}{\mu_{I-MF}} \tag{45}$$

$$U_{I-MF} = \lambda_{I-MF} \cdot I_{I-MF} \tag{46}$$

The PMs of Inverter with R-L Load with MF can be obtained similarly from Eqns. (11) to (15) respectively.

### 2.4.3 Overall Evaluation of BPIs and PMs for RL-Load

The overall equivalent failure rate of inverter will be

$$EFR = \lambda_I = \lambda_{I-NNF} + \lambda_{I-MF} \tag{47}$$

The overall Average annual outage time of inverter will be

$$AAOT = U_I = U_{I-NNF} + U_{I-MF} \tag{48}$$

and the overall Mean outage time of inverter will be

$$MOT = r_I = \frac{U_I}{\lambda_I} \tag{49}$$

The overall PMs of Inverter with R-L Load will be obtained similarly from Eqns. (11) to (15) respectively.

### 3. Results and Discussion

Data of components for Inverter are taken from [1].

- ❖ SCR →  $\lambda = 4.7043 \times 10^{-5}$  failures / year.
- ❖ Diode →  $\lambda = 25.63 \times 10^{-5}$  failures / year.
- ❖ Capacitor →  $\lambda = 0.0212$  failures / year.
- ❖ It is assumed the probability of the voltage source is unity. i.e., No failure of the voltage source is considered.
- ❖ Repair rate is assumed to be equal for all the components. i.e.,  $r = 1.062$  hrs / year.

#### 3.1 Based on Operation of Single Phase Full Bridge Inverter with R-Load

There are three cutsets have been obtained i.e., C<sub>1</sub>, C<sub>2</sub> and C<sub>3</sub>.

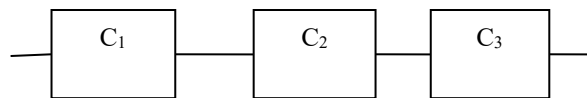


Fig. 9 PLD of a single phase inverter with R-Load

Cutset-1:  $\lambda_{C_1} = 0.0212$  failure/year ;  $r_{C_1} = 1.062$  hrs.

Cutset-2:  $\lambda_{C_2} = \lambda_{S_3} + \lambda_{S_4} = 9.408 \times 10^{-5}$  failure/year

$$U_{C_2} = \lambda_{S_3} r_{S_3} + \lambda_{S_4} r_{S_4} = 9.9918 \times 10^{-5} \text{ hrs/year.}$$

$$r_{C_2} = 1.0619 \text{ hrs ;}$$

Cutset-3:  $\lambda_{C_3} = \lambda_{S_1} + \lambda_{S_2} = 9.408 \times 10^{-5}$  failure/year.

$$U_{C_3} = \lambda_{S_1} r_{S_1} + \lambda_{S_2} r_{S_2} = 9.9918 \times 10^{-5} \text{ hrs/year.}$$

$$r_{C_3} = 1.0619 \text{ hrs ;}$$

All three cut sets are in series configuration, the equivalent of PLD is shown in Fig. 10. The BPIs of Inverter with R-Load is as follows, presented in Table 1, and plotted as bar chart in Fig. 11.



Fig. 10 Final equivalent of PLD for R-Load

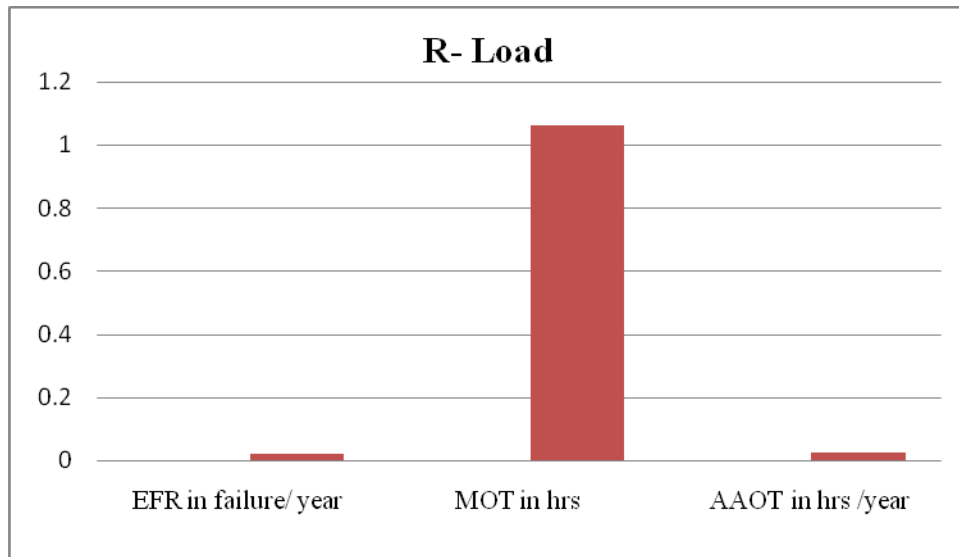
$$EFR = \lambda_1 = \lambda_{c_1} + \lambda_{c_2} + \lambda_{c_3} = 0.0214 \text{ Failure/year.}$$

$$AAOT = U_1 = \lambda_{c_1} r_{c_1} + \lambda_{c_2} r_{c_2} + \lambda_{c_3} r_{c_3} = 0.0227 \text{ hrs/year}$$

$$MOT_{of\ Inverter} = r_1 = \frac{U_1}{\lambda_1} = 1.061 \text{ hrs.}$$

**Table 1** BPIs of inverter with R-Load

BPI of Inverter as per the operation	EFR in failure/ year	MOT in hrs	AAOT in hrs /year
	0.0214	1.061	0.0227



**Fig. 11** BPIs of inverter with R-Load

The PMs for inverter with R-Load are computed using Eqns. (11) to (15) respectively as follows, presented in Table 2, and shown in Fig. 12.

**Table 2** PMs of inverter with R-Load

S. No.	Probabilistic Measures	Inverter with R-Load
1	MTTF in years	46.7289
2	MTTR in hrs	1.061
3	MTBF in years	46.7290
4	Availability	0.99999764
5	Unavailability	0.00000236

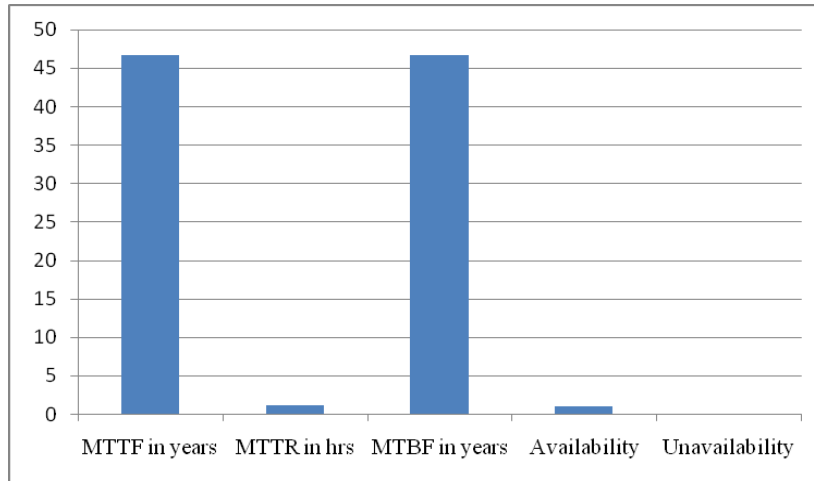


Fig. 12 PMS of inverter with R-Load

### 3.2 Based on Operation of Single Phase Full Bridge Inverter with R-L Load with NMF

There are five cutsets have been obtained i.e., C<sub>1</sub>, C<sub>2</sub>, C<sub>3</sub>, C<sub>4</sub>, C<sub>5</sub> respectively and shown in Fig. 13.

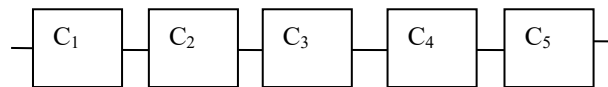


Fig. 13 Probability logic diagram for R-L load without malfunction

Cutset-1:  $\lambda_{C_1} = 0.0212$  failure/year;  $r_{C_1} = 1.062$  hrs.

Cutset-2:  $\lambda_{C_2} = \lambda_{S_1} + \lambda_{S_2} = 9.408 \times 10^{-5}$  failure/year.

$$U_{C_2} = \lambda_{S_1} r_{S_1} + \lambda_{S_2} r_{S_2} = 9.9918 \times 10^{-5} \text{ hrs/year.}$$

$$r_{C_2} = 1.0619 \text{ hrs.}$$

Cutset-3:  $\lambda_{C_3} = \lambda_{D_3} + \lambda_{D_4} = 51.26 \times 10^{-5}$  failure/year.

$$U_{C_3} = \lambda_{D_3} r_{D_3} + \lambda_{D_4} r_{D_4} = 54.438 \times 10^{-5} \text{ hrs/year.}$$

$$r_{C_3} = 1.0619 \text{ hrs.}$$

Cutset-4:  $\lambda_{C_4} = \lambda_{S_3} + \lambda_{S_4} = 9.408 \times 10^{-5}$  failure/year.

$$U_{C_4} = \lambda_{S_3} r_{S_3} + \lambda_{S_4} r_{S_4} = 9.9918 \times 10^{-5} \text{ hrs/year.}$$

$$r_{C_4} = 1.0619 \text{ hrs.}$$

Cutset-5:  $\lambda_{C_5} = \lambda_{D_1} + \lambda_{D_2} = 51.26 \times 10^{-5}$  failure/year.

$$U_{C_5} = \lambda_{D_1} r_{D_1} + \lambda_{D_2} r_{D_2} = 54.438 \times 10^{-5} \text{ hrs/year.}$$

$$r_{C_5} = 1.0619 \text{ hrs.}$$

All Five cut sets are in series configuration, the equivalent of PLD of inverter without malfunction is shown in Fig. 14. The BPI of Inverter with R-L Load is as follows and shown in Table 3.



Fig. 14 Final equivalent of PLD for RL-Load

$$\begin{aligned}
 EFR &= \lambda_{I-NMF} = \lambda_{C_1} + \lambda_{C_2} + \lambda_{C_3} + \lambda_{C_4} + \lambda_{C_5} = 0.0244 \text{ failure/year}; \\
 AAOT &= U_{I-NMF} = \lambda_{C_1} r_{C_1} + \lambda_{C_2} r_{C_2} + \lambda_{C_3} r_{C_3} + \lambda_{C_4} r_{C_4} + \lambda_{C_5} r_{C_5} = 0.0237 \text{ hrs/year} \\
 MOT \text{ of Inverter} &= r_{I-NMF} = \frac{U_{I-NMF}}{\lambda_{I-NMF}} = 1.058 \text{ hrs};
 \end{aligned}$$

The PMs for single phase inverter with R-L Load for NMF are computed using Eqns. (11) to (15) respectively as follows and shown in Table 4.

### 3.3 Based on Operation of Single Phase Full Bridge Inverter with R-L Load with MF

There are Four cutsets have been obtained i.e., C<sub>1-MF</sub>, C<sub>2-MF</sub>, C<sub>3-MF</sub> and C<sub>4-MF</sub>, and shown in Fig. 15.

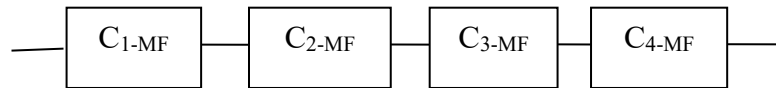


Fig. 15 PLD of inverter considering malfunctioning

All four Cutsets are in series, Unreliability of the inverter considering malfunctioning of SCRs obtained from Eqn. (37) is as follows:

$$\begin{aligned}
 Q_{I-MF} &= Q_{C_1} + Q_{C_2} + Q_{C_3} + Q_{C_4} = 0.000048 \times 4 = 0.000576 \\
 R_{I-MF} &= (1 - Q_{I-MF}) = (1 - 0.000576) = 0.99942
 \end{aligned}$$

The transitional rate of the inverter during malfunctioning is obtained from Eqn. (39) is as follows:  $\mu_{I-MF} = 0.97271$  hrs. The BPIs of Inverter with Malfunction are computed as follows and shown in Table. 3.

$$\begin{aligned}
 \lambda_{I-MF} &= \lambda_{C_1-MF} + \lambda_{C_2-MF} + \lambda_{C_3-MF} + \lambda_{C_4-MF} = 5.645 \times 10^{-4} \text{ failures/year}; \\
 r_{I-MF} &= \frac{1}{\mu_{I-MF}} = 1.028 \text{ hrs}; \\
 U_{I-MF} &= \lambda_{I-MF} \cdot r_{I-MF} = 5.8033 \times 10^{-4} \text{ hrs/year};
 \end{aligned}$$

The PMs for Inverter with R-L Load for MF can be computed similarly using Eqns. (11) to (15) respectively as follows and shown in Table.4.

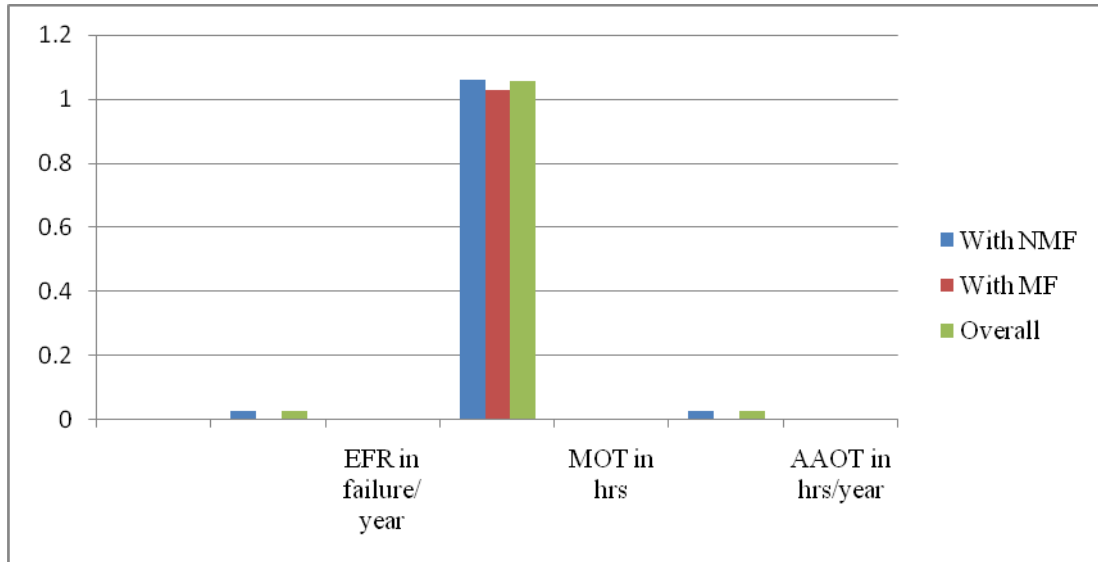
### 3.4 The Overall Evaluation of BPIs and PMs for Inverter with R-L Load

The overall BPI for inverter with R-L Load are computed using the Eqns. (47) to (49) as follows, presented in Table 3, and shown in Fig. 16.

$$\begin{aligned}
 EFR &= \lambda_1 = \lambda_{I-NMF} + \lambda_{I-MF} = 0.02296 \text{ failure/year.} \\
 AAOT &= U_1 = U_{I-NMF} + U_{I-MF} = 0.02428 \text{ hrs/year.} \\
 MOT &= r_1 = \frac{U_1}{\lambda_1} = 1.0575 \text{ hrs.}
 \end{aligned}$$

Table 3 BPIs of inverter with R-L load for NMF, MF and overall

S. No.	BPI of Inverter as per the operation	EFR (in failure/ year)	MOT (in hrs)	AAOT (in hrs/year)
1	With NMF	0.0244	1.0580	0.0237
2	With MF	5.645x10 <sup>-4</sup>	1.0280	5.8033x10 <sup>-4</sup>
3	Overall BPI for Inverter	0.02296	1.0575	0.02428

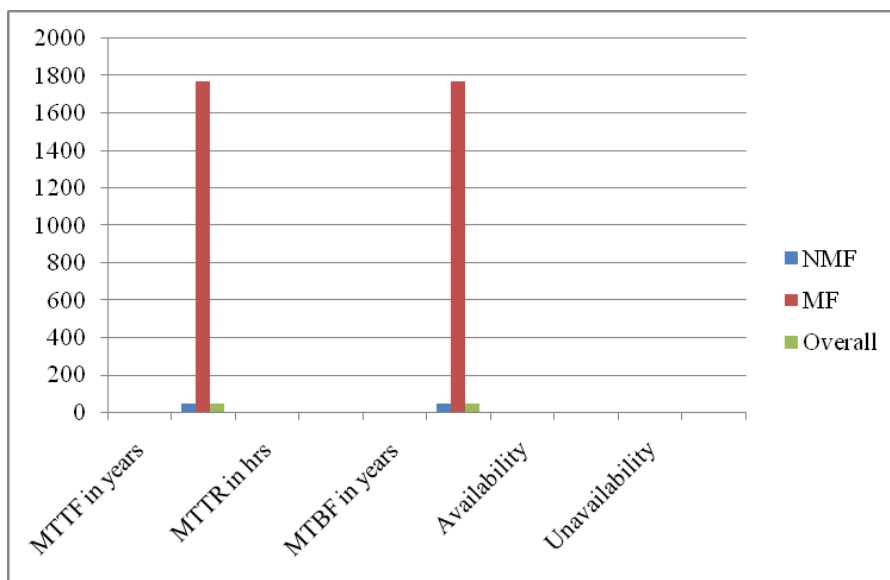


**Fig. 16** BPIs of inverter with R-L load for NMF, MF and overall

The overall PMs for Inverter with R-L Load are computed using similarly Eqns. (11) to (15) respectively as follows, presented in Table 4, and shown in Fig. 17.

**Table 4** PMs of inverter with R-L load for NMF, MF and overall

S. No.	PMs	NMF	MF	Overall
1	MTTF in years	44.6428	1771.4791	43.5540
2	MTTR in hrs	1.058	1.028	1.0575
3	MTBF in years	44.6429	1771.4792	43.5541
4	Availability	0.99999779	0.999999943	0.9999977
5	Unavailability	0.00000223	0.000000057	0.0000023

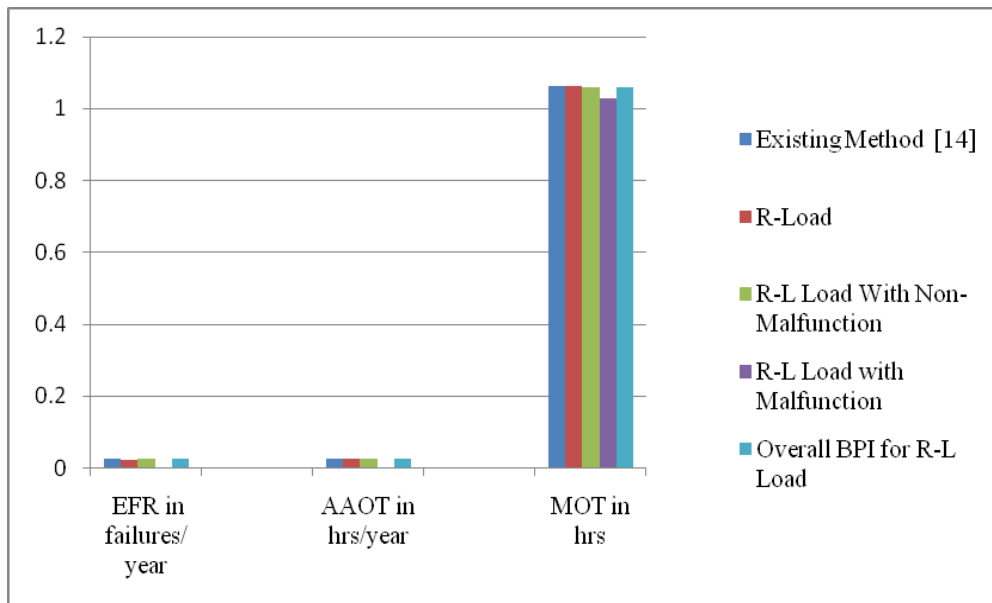


**Fig. 17** PMs of inverter with R-L load for NMF, MF and overall

The results obtained are presented in Tables. 5 and 6 for BPIs and PMs for Inverter configuration with R-L Load. The results are also compared with the existing method [9] and the proposed method, and presented in Figs. 18 and 19 respectively.

**Table 5** BPIs for inverter with existing and proposed method

S. No.	BPI Indices	Existing Method [9]	Proposed Cutset Method			
			R-Load	R-L Load With Non-Malfunction	R-L Load with Malfunction	Overall BPI for R-L Load
1	<b>EFR</b> (in failures/year)	0.0224	0.0214	0.0224	$5.645 \times 10^{-4}$	0.02296
2	<b>AAOT</b> (in hrs/year)	0.0237	0.0227	0.0237	$5.8033 \times 10^{-4}$	0.02428
3	<b>MOT</b> (in hrs)	1.061	1.061	1.0580	1.0280	1.0575



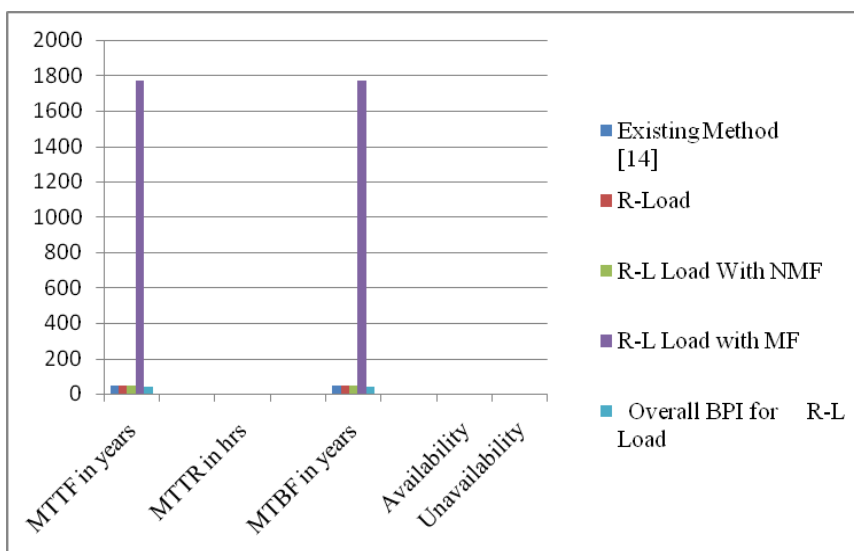
**Fig. 18** BPIs for inverter with existing and proposed method

It has been observed from Table 5 that, for R-L load, when there is no malfunctioning effect, BPIs are same as that of the series configuration although cutset models have been developed based on the operation of the inverter. Further, including malfunctioning of the SCRs, it is observed that EFR and AAOT of the BPIs will increase i.e. the reliability will decrease which is more realistic. Further, it is also observed that MOT is more or less same.

The modelling of PLDs using cutset has been developed and it is observed that the Equivalent Failure Rate and Average Annual Outage Time are decreased while there is no change in Mean Outage Time. This is due to the fact that the failure of diodes will not affect the operation of the inverter.

**Table 6** PMs for inverter with existing and proposed method

S. No.	Probabilistic Measures	Existing Method [9]	Proposed Cutset Method			Overall BPI for R-L Load
			R-Load	R-L Load With NMF	R-L Load with MF	
1	MTTF (in years)	44.6428	46.7289	44.6428	1771.4791	43.5540
2	MTTR (in hrs)	1.061	1.061	1.058	1.028	1.0575
3	MTBF (in years)	44.6429	46.7290	44.6429	1771.4792	43.5541
4	Availability	0.99999776	0.99999764	0.99999776	0.999999943	0.9999977
5	Unavailability	0.00000224	0.00000236	0.00000224	0.000000057	0.0000023



**Fig. 19** PMs for inverter with existing and proposed method

From Table 6, it can be concluded that the PMs MTTF and MTBF will be increasing for R-Load, which means that operating time of the inverter will be more. This is due to the fact that there will be no effect of the diode in the R-Load in inverter configuration. Further, MTTF, MTBF decreases from the existing method for R-L Load as compared to the proposed method considering the overall indices including the effect of malfunctioning of the SCRs.

#### 4. Conclusions

In this paper, the modelling of single phase full bridge inverter using cutsets has been developed for computation of BPIs and PMs for R and R-L Loads. Considering the operation strategies of the inverter, four modes of operation has been considered and PLDs have been developed, where as in the existing method series network configuration of PLD only and considered irrespective of the load conditions [9].

## Acknowledgements

The Authors extended their appreciation Sri Krishnadevaraya University College of Engineering and Technology Ananthapuramu and Jawaharlal Nehru Technological University, Ananthapuramu for providing support and facilities to my research work.

## Conflict of Interest

The authors declare that there is no conflict of interest.

## Authors Contribution

*All authors contributed equally to the conceptualization, formal analysis, investigation, methodology, and writing and editing of the original draft. All authors have read and agreed to the published version of the manuscript.*

## References

- [1] U.S. DOD, Military Standard Handbook, MIL STDHBK 217F, Reliability Prediction of Electronic Equipment Washington, DC, Dec. 1991.
- [2] V. Sankar, V. C. Prasad, K. S. Prakasa Rao, "A Cutset approach for HVDC Converter Reliability Evaluation", International Conference on Advances in Power System Control, Operation and Management, APSCOM-1991, pp.757-761.
- [3] Hongbin Li and Qing Zhao "A Cut/Tie Set Method for Reliability Evaluation of Control Systems", American Control Conference, June 8-10, 2005. Portland, OR, USA, pp. 1048-1053.
- [4] Reliability Evaluation of Engineering systems, R. Billinton, R.N. Allan, Springer (India) Private Limited, Second Edition-2012.
- [5] Reliability Evaluation of Power Systems, R. Billinton, R.N.Allan, Springer (India) Private Limited, Second Edition-2012.
- [6] Power Electronics: Device, Circuits and Applications, Muhammad H. Rashid, Fourth Edition. Published by Prentice Hall India, 2008.
- [7] Balak Abdi, Amir Hussein Ranjbar, Gevorg B, Ghareh Petian, Jafar Milimonfored, "Realibility Considerations for parallel Performance of semiconductor switches in High-Power switching power supplies", IEEE Transactions on Industrial Electronics, Vol. 5, No. 6, June 2009, pp. 233-239.
- [8] Moammed Benidris, Oydeep Mitra, Salem Elsaials, "A Method for Reliability Evaluation of Multi-Level Converters", IEEE-Transactions on Industrial Electronics, Vol. 5, pp. 1422-1429, January 2012.
- [9] Peng Zhang, Yang Wang, Weidong Xiao, Wenynan Li, "Reliability Evaluation of Grid Connected Power systems", IEEE Transactions on Sustainable Energy, Vol.3, No. 3, pp.379-389, July 2012.
- [10] D.Ravi Kumar and V.Sankar "Approximate system Reliability analysis of Power system Network using Cutsets", i-Manager's journal on Power Systems Engineering, Vol.2, Issue.3, pp.11-21, (Aug-Oct)-2014.
- [11] System Reliability Concepts, V. Sankar, Himalaya Publications Pvt. Ltd., 2015.
- [12] Baoping Cai, Youghong Liu and Zengkai Liu, "A Framework for the reliability evaluation of grid-connected photovoltaic systems in the presence of intermittent faults", Elsevier, Energy 93, pp.1308- 1320, November 2015.
- [13] Ariya Sangwongwanich, Yougheng Yang, Dezso Sera, Frade Blaabjerg and Dao Zhou " On the Impact of PV array sizing on the inveter Reliability and Life-Time", IEEE Transactions on Industry Applications, Vol.99, pp.1-12, April 2018.
- [14] El ysaouy Laheen, Lahbabi Mohammed and Oumnad Abdelmajid, "Enhacing the Performances of PV Array Configurations under partionally shaded conditions: A Comparative Study", International Journal of Renewable Energy Research, Vol. 8, No. 3, pp.1779-1770, May 2018.
- [15] Jack Flicker, Jay Johnson, Peter Hacke and Ramanathan Thiagarajan, "Automating Component - Level Stress Measurements for Inverter Reliability Estimation", Energies 2022, pp.1-15, July 2022. <https://doi.org/10.3390/en15134828>
- [16] Bharathi Rao and M. Satyendra Y.Zhao, Y.Che, T.Lin, C. Wang, J.Lin, J.Xu, and J.Zhou, "Minimal cut sets-based Reliability Evaluation of the More Electric Aircraft Power Systems", Mathematical Problems in Engineering, Vol. 5, pp. 1-11, January 2018.
- [17] Palthur Shashavali and V. Sankar, "Reliability Evaluation of Photo Voltaic Array using Cutsets," International Journal of Recent Technology and Engineering, Vol. 8, Issue. 3, pp. 5197-5201, September 2019. (Scopus)
- [18] <https://www.ijrte.org/wp-content/uploads/papers/v8i3/C5816098319.pdf>
- [19] Eyyup Demirkutlu and Ires Iskender, "Grid Connected Three-Phase Boost-Inverter for Solar PV Systems," International Journal of Renewable Energy Research, Vol. 11, No. 2, June 2021, pp. 776-784.

- [20] V. Rajini and A. Magdalene, "Investigations on Interleaved and Coupled Split-Pi DC-DC Converter for Hybrid Electric Vehicle Applications," *International Journal of Renewable Energy Research*, Vol. 11, No. 2, June 2021, pp. 808-817.
- [21] Shahd Fadhil Jaber and Amina Shakir, "Design and Simulation of a Boost-Micro inverter for Optimized Photovoltaic System Performance," *International Journal of SMART GRID*, Vol. 5, No. 2, pp. 94-102, June 2021.
- [22] Palthur Shashavali and V. Sankar, "Evaluation of Probabilistic Measures of Interleaved Flyback Inverter with Switched Redundancy and Cutset Concepts," *The flagship international conference series of the IEEE Madras Section*, 27 – 28 August 2021, MASCON 2021.
- [23] Palthur Shashavali and V. Sankar, "Switched Redundancy and Cutset Approach to Estimate Basic Probability Indices of Interleaved DC-DC Boost Converter," *International Journal of Renewable Energy Research*, Vol. 11, No. 3, pp. 1281-1291, September 2021. (ESCI & Scopus) (Impact Factor: 5.127)
- [24] <https://www.ijrer.org/ijrer/index.php/ijrer/article/view/12188>
- [25] Kumar, "Reliability analysis of single phase quasi Z source inverter for standalone photovoltaic system", *Bulletin of Electrical Engineering and Informatics*, Vol. 11, No. 6, pp. 3023-3032, December 2022. <https://doi.org/10.11591/eei.v11i6.4101>
- [26] Sainadh Singh Kshatri et. al., "Reliability Assessment of Hybrid Silicon-Silicon Carbide IGBT Implemented on an Inverter for Photovoltaic Applications", *Journal of New Materials for Electrochemical systems*, Vol. 26, No. 1, pp.1-6, January 2023. <https://doi.org/10.14447/jnmes.v26i1.a01>
- [27] Bo Zhang and Yuan Gao, "IGBT Reliability analysis of photovoltaic inverter with reactive power output capability", *Microelectronics Reliability*, Vol. 147, pp.1-9, August 2023.
- [28] <https://doi.org/10.1016/j.microrel.2023.115073>
- [29] Ranjith Kumar Gatla, M Ramesh, Kota Prasada Rao, P Shashavali, Durga Prasad Garapati, P Chandra Babu and Devineni Gireesh Kumar, "Effect of Junction Temperature on System Level Reliability of Grid Connected PV Inverter", *Journal of New Materials for Electrochemical systems*, Vol. 26, No. 4, pp.248-256, October 2023. <https://doi.org/10.14447/jnmes.v26i4.a03>
- [30] Niraj Kumar Dewangan, · Deepak Verma, Rakeshwri Agrawal, · Dhananjay Kumar and Krishna Kumar Gupta, "Reliability evaluation of a novel fault tolerant multilevel inverter with reduced components", *Electrical Engineering (2023)* 105:1655–1668, February 2023.
- [31] <https://doi.org/10.1007/s00202-023-01744-3>
- [32] Rouzbeh Haghghi, Van-Hai Bui, Mengqi Wang and Wencong Su, "Survey of Reliability Challenges and Assessment in Power Grids with High Penetration of Inverter-Based Resources", *Energies* 2024, 17(21), 5352, October 2024. <https://doi.org/10.3390/en17215352>