

The Investigation of Die Back Edge Cracking in Flip Chip Ceramic Ball Grid Array Package (FC-CBGA)

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Abstract

The cracking between die back edge and top fillet for Flip Chip Ceramic Ball Grid Array (FC-CBGA) package due to thermal cycling have been investigated in this study. Finite Element Analysis (FEA) model was used to analyze the effect of fillet geometry and material properties of underfill upon stresses along the die back edge. The thermo-mechanical properties of commercial underfill were obtained by using Thermal Mechanical Analyzer (TMA) and Dynamic Mechanical Analyzer (DMA) as the input for the simulation. Die stress distribution for different fillet height and width were generated to depict variation of stress due thermal loading and the variations of tensile stress were discussed for parameter optimization. The effect of different underfill material properties were discussed as well for thermal stress reliability improvement.

Keywords: FC-CBGA; Die Edge Crack; Underfill Fillet; Thermal Stress; Underfill Properties

1 INTRODUCTION

Flip chip method becomes popular technology in electronic packaging industry because of small package size and high electrical performance. However, in order for flip chip technology to become more popular, there are several hurdles to overcome, for instance, assembly cost and reliability issues. The cost issue is the most critical parameter among them. Therefore, the trend is that substrate material for flip chip changes from ceramic to organic due to cost issue. However, ceramic substrate (FC-CBGA) offers better moisture resistance, electrical insulating property and higher thermal conductivity than organic substrate (FC-PBGA) and still relevant until now especially for high-reliability commercial applications like CPU [1].

Management of thermo-mechanical stress and improvement of moisture susceptibility are key challenges in reliability assessment of FC-PBGA. The thermo-mechanical stresses are mainly induced by large Coefficient Thermal Expansion (CTE) mismatch between silicon die and organic substrate which cause the typical failure mode such as underfill and die cracking, interface delamination and solder fatigue cracking [2]. An application of ceramic substrate has lower risk failure especially underfill crack underneath the die area due to lower CTE mismatch between die and substrate as compared to organic substrate [3].

Underfill cracking can be categorized into three groups such as popcorn cracking, underfill corner cracking, and underfill edge cracking which can lead to die edge cracking [4]. The popcorn and corner cracking in underfill were originated from interfacial delamination between underfill and die passivation [4]. Since the mechanism of die edge cracking due to thermal loading or temperature cycle test (TCT) was very complicated, mechanical simulations and experiments were conducted and studied elsewhere [5]. The die edge cracking was closely related to the local CTE mismatch between underfill material and silicon die. Die edge cracking can be eliminated by using low CTE underfill material and by the control of underfill fillet size [4]. It also suggested that to avoid die edge crack issue during TCT, the thickness of die and substrate should be changed in order to reduce the package warpage [6]. In addition, the stiffness should be attached on the backside of silicon die to release the stress.

Since for electronic packaging industry, to change the thickness of die and substrate is almost impossible to implement due to standard geometry produced by supplier, the only option to eliminate the die edge crack is by adjusting the fillet geometry of underfill or attach the stiffener on the backside of silicon

die. Fillet geometry variability is the result of the underfill dispensing process and the materials involved. Underfill is usually dispensed on one or two adjacent sides of the die. Capillary flow is utilized to fill the gap between die and substrate, in the presence of flip chip bumps where the viscosity, surface tension and contact angle are key material properties [5]. The interaction of the substrate and underfill affects the fillet formation. Differences in surface roughness, cleanliness, solder mask properties on the substrate and passivation properties on the silicon die could result in very different fillet geometries for the same underfill. Lim et al. [6] attempted to quantify the effect of underfill fillet on flip chip package reliability for ceramic flip chip packages with wafer level underfill for a standard die. For ceramic packages the underfill delamination jeopardy is less due to closely matched coefficients of thermal expansion of the ceramic substrate and the silicon die. Therefore the failures after reliability test such as thermal cycling loading are very rare happened. Recently, it was found in industry the crack occurred at die back edge adjacent to top fillet that propagated to active area due to thermal stress in FC-CBGA.

In this paper the thermal stress-analysis using FEA was carried out to investigate the source of die back edge crack occurred between die edge and underfill top fillet of package FC-CBGA due to thermal cycling reliability test (-55°C/125 °C) at 500 times cycle. Package with different underfill's thermo-mechanical properties material such as the CTE, Young's modulus and the glass transition temperature (Tg) have been simulated to find improvement of die back edge reliability.

2 METHODOLOGY

2.1 Underfill Bulk Analysis- Data Properties for Simulation

Material analysis using dynamic mechanical analysis (DMA) and thermo-mechanical analysis (TMA) have been conducted for obtaining underfill material's properties [7]. Linear thermal expansion coefficient of cured samples was measured from -50°C to 260°C using a TA Instrument thermal mechanical analyzer operated in expansion mode. Cylindrical samples with a typical sample height of approximately 4.7 mm were cured at 165°C at 60 minute. TMA data were obtained at a heating rate of 58°C/ min. To minimize viscous flow during the measurements especially above the glass transition temperature Tg, all TMA measurements were performed with a small loading force of 5 mN.

Dynamic mechanical properties such as Young Modulus of cured samples were determined from -50°C to 210°C using DMA techniques. DMA

measurements were carried out on a TA Instrument dynamic mechanical analyzer operated in rectangular tension mode. The dimension of cured sample was 15.0 mm x 12.9 mm x 1.20 mm. During DMA experiments, the static force was kept at 120% of the dynamic force and the frequency of the dynamic force was maintained at 1 Hz. The result of TMA and DMA analysis is shown in Table 1.

Table 1 : DMA and TMA properties for all components of FC-CPGA

Component	DMA Modulus (GPa)	CTE 1/2 (ppm/°C)	TMA Tg (°C)
Die	131	2.8	-
Underfill	Refer to TABLE 2		
Ceramic Substrate	75	12.3	-

2.2 Reliability test for unit package

The package was based on Hi-CTE ceramic substrate with size of 33mm x 33mm and thickness of 1.2 mm. The die attached to the substrate was a flip chip with 1023 inputs/outputs and has size of 14.5mm x 11.9mm and thickness of 0.75mm. The flux used was a clean type; reflow has been performed under an N₂ atmosphere. The commercial underfill from Ablestik was used for filling the gap between die and substrate by using conventional capillary flow dispenser. The 2D dimension of the package assembly is given in Figure 1. For evaluating assembly reliability, temperature cycling with a two-chamber air-to-air test (ATC) between -55°C and 125°C for 500 cycles has been performed and the failure observation was executed by using C-Mode Scanning Acoustic Microscope (C-SAM). Since the package was ceramic substrate, the preconditioning test (Moisture Sensitivity Level Test) was not necessary.

Die

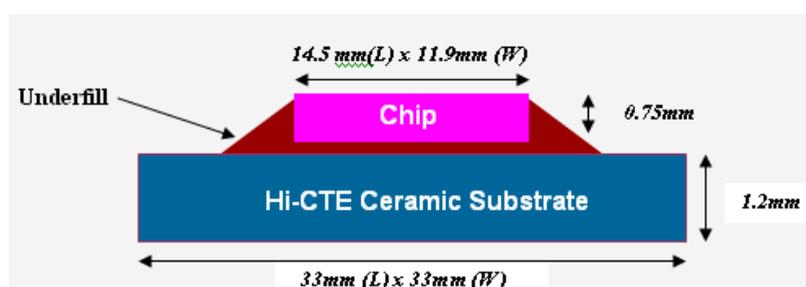


Figure 1 : Schematic diagram of 2D dimension of FC-CBGA

Through C-SAM inspection, it was found the unit with die edge crack after 500 cycles thermal loading. Figure 2 shows the position of the die edge crack. The source of die crack has been investigated by using finite element analysis (FEA) simulation.



Figure 2 : Die edge crack detected in package after 500 times thermal cycling test

2.3 Finite Element Modeling

To study the crack problem, a commercial finite element code, namely, ANSYS was employed. A three-dimensional model was established as shown in Figure 3 (a) to simulate the package under ATC test. It should be noted only die, substrate and underfill were modeled in the finite element analysis. Besides, due to the symmetry in the assembly structure, only quarter of package was considered [8]. The material properties used in the computational modeling are given in Table 1 and 2.. The solder bump considered ignored in this analysis since the analysis was focused only to die back edge. Neglecting them won't affect so much of global model results. All constituents were considered as linear elastic materials. The scope of the present study was to investigate the effects of underfill fillet geometry. The details of fillet configuration is shown in Figure 3 (b) where W is underfill fillet width and H is underfill fillet height. There were three different fillet heights (50%, 75% and 100%) and five different fillet width (0.25mm, 0.75mm, 1.25mm, 1.75mm and 2.25mm) were selected for this studies. Figure 4 shows the schematic diagram of five different filler width and underfill contact angle respectively.

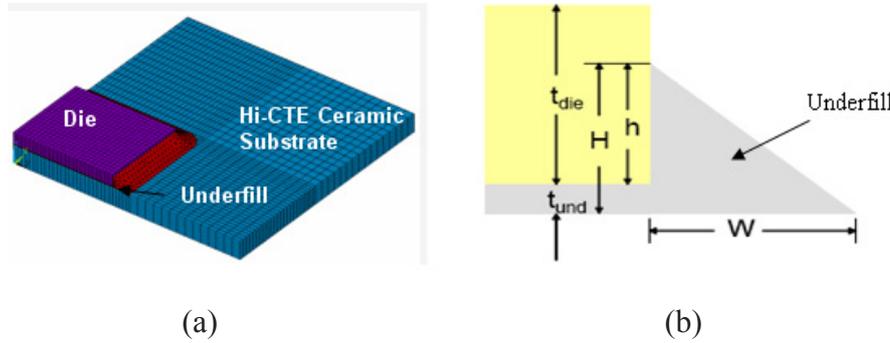


Figure 3 : (a) Quarter Symmetric FE model of FC-CBGA
(b) Details of fillet configuration [3]

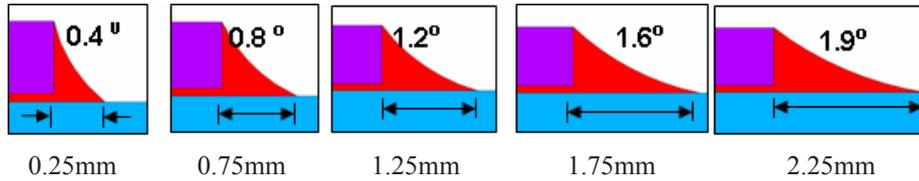


Figure 4 : Schematic diagram for fillet width and angle geometry at 100% fillet height

Three different combinations of CTE and Young's modulus also were used to observe the effect of material properties against thermal stress distribution. 100% fillet height and 1.61mm fillet width was assumed for all materials.

While two CTE values before and after the glass transition temperature T_g , CTE_1 for $T < T_g$ and CTE_2 for $T > T_g$, are usually provided by experimental results for polymer materials, the ANSYS definition of the mean or effective expansion coefficient was used for the implementation:

$$Eff.CTE = \frac{(T_g - T_1)CTE_1 + (T_2 - T_g)CTE_2}{T_2 - T_1} \quad (1)$$

where T_2 is the stress-free or reference temperature of the component being modeled [7]. As T_1 was a -55°C and T_2 was the underfill's curing temperature (165°C), the effective CTE for underfill was calculated and shown below:

Table 2 : Thermo-mechanical properties of underfill for FEA input

Underfill	DMA E (GPa)	CTE ₁ /CTE ₂ (ppm/C)	TMA Tg (°C)	Eff. CTE (ppm/°C)	E x CTE
Ablestik	9.7	29.3 / 92.8	102.35	47.4	459.78
Henkel	10.1	25.4 / 85.4	105.82	41.5	419.15
Nagase	5.9	34.8/126.2	97.5	61.36	362.02

3 RESULT AND DISCUSSION

3.1 Effect of Fillet Height of Underfill

The die was detached from the global model to observe the die back stress distribution only. Figure 5, 6 and 7 show the thermal shear stress distribution at the die back for fillet height of 100%, 75% and 50% respectively. From the simulation results the highest shear stresses were always found at the edge of die. For 100% fillet height (Figure 5), the in-plane normal stresses S_x and S_y were tensile at die back center, and are highest near edges. The out-of-plane normal stress S_z was near-zero at die back center, but becomes tensile near edges. The maximum principal stress S_1 was tensile at die back center, and was highest near corners. Correlating with actual observation, die cracks are more likely to happen near edges due to higher tensile stress. This stress was believed can lead delamination at the interface between underfill fillet and die. Later the delamination was propagated to die edge and initiated the crack [7].

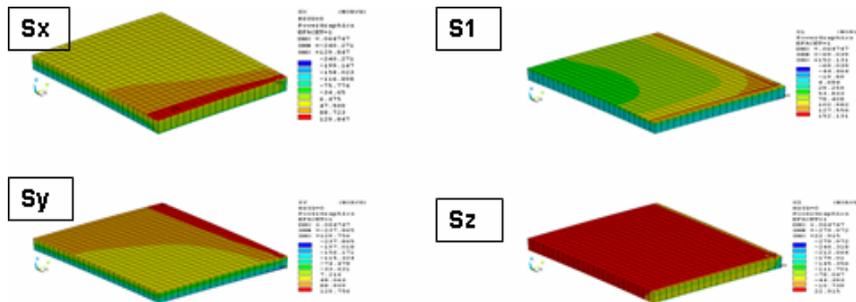


Figure 5 : Die stress distribution for fillet height 100%

For 75% fillet height (Figure 6), the in-plane normal stresses S_x and S_y were tensile at die back center, and were highest near edges. The out-of-plane normal stress S_z was near-zero at die back center, but became tensile near edges. The maximum principal stress S_1 was tensile at die back center, and was highest near corners. Tensile stresses were lower than that of 100% fillet height.

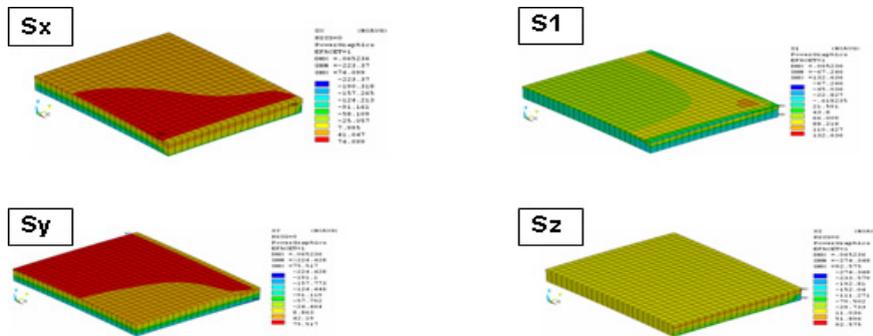


Figure 6 : Die stress distribution for fillet height 75%

For 50% fillet height (Figure 7) all stress distribution pattern showed the same as Figure 5 and 6 for in/on plane normal stress and maximum principal stress but this fillet height giving lower tensile stresses that that of 75% and 100% fillet height.

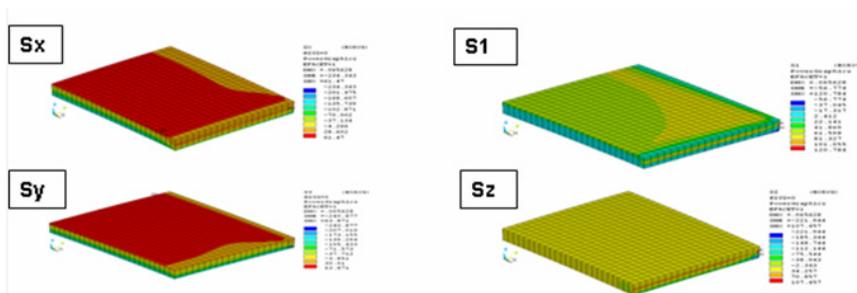


Figure 7 : Die stress distribution for fillet height 50%

The result at above agreed with investigation done by Karan et al. [3]. He found that the shear stress was increased when the fillet height increased. According to analysis of shear stress at different locations of interest in underfill fillet area as depicted in Figure 8, location 1 (die bottom/corner underfill) and location 2 (die edge/underfill fillet top) probably have large delamination compared with location 3 based on their adhesion at the interfaces. Typically adhesion between materials at location 1 (passivation and underfill) is weaker than location 2 (silicon and underfill) [10]. Experience and data suggests that location 1 is the most likely location for stress driven delamination [9].

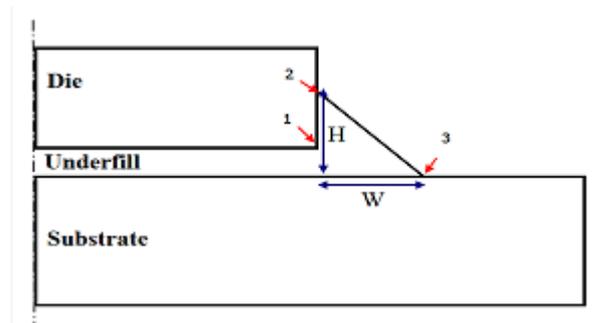


Figure 8 : Location of interest in underfill fillet area [3]

For further analysis the tensile stress variation with fillet height coverage for principal stress S1 was plotted as depicted in Figure 9. It was clearly demonstrated the highest stress occurred at the corner and edge of die where decreases the fillet height will decrease the maximum stress.

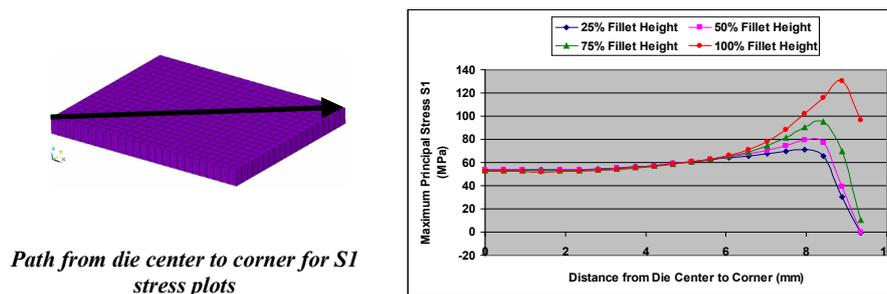


Figure 9 : Tensile stress variations with different fillet height (1.61mm fillet width)

3.2 Effect of Fillet Width of Underfill

Figure 10 shows the effect of fillet width upon maximum tensile stress. Die back edge tensile stresses increases with increasing fillet width or die/epoxy contact angle. Since the stress singularity becomes more severe as the epoxy fillet contact angle increases, it is suggested that in the manufacturing process this angle be kept as small as possible [9]. Therefore interaction of fillet height and width are very vital to keep the stress especially at the edge of die as low as possible in avoiding crack occurs. Karan [3] found that tensile and shear stress highest at the top of the die and underfill interface where fillet width was dominant control factor instead of fillet height. This is supported by estimation value of maximum tensile stress in Figure 8 with fillet width, 1.61mm was larger than value of maximum stress for 100% fillet height as in

Figure 7. Lei et al. [9] mentioned that when the fillet angle was decreased, the energy release rate was significant released.

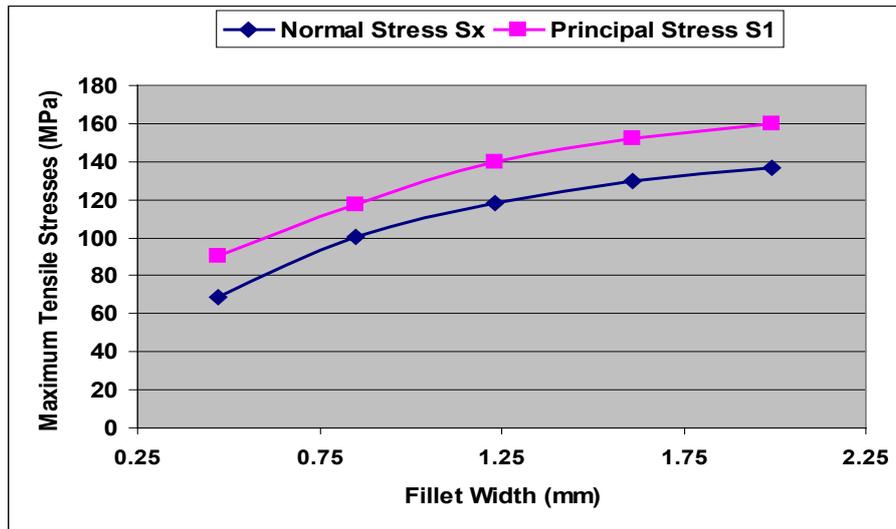


Figure 10 : Maximum tensile stress with different fillet width (100% Fillet Height)

3.3 Effect of Underfill Material Properties

Figures 11 illustrates the resulting maximum first principal tensile stress S1 with in-plane normal stress Sx, on the quarter symmetric die for three underfill materials as comparison. It is observed that underfill from Henkel and Nagase give about 10% and 13% respectively lower die back maximum tensile stresses in comparison with underfill from Ablestik (material that give crack). This indicated that the material from Nagase and Henkel with lower tensile stress has minimum risk of die edge cracking compared to Ablestik. In addition, low multiplication of modulus and CTE ($E \times CTE$) helps to reduce die stresses, as shown in Table 2 [8]. In most cases high value of $E \times CTE$ give high die corner shear stress, which may to delamination between the interface of die/underfill and further give impact to the fragile layer of low-K interlevel dielectric (ILD) which located near to surface of silicon die [10]. From Figure 11, underfill from Nagase, which has lowest ($E \times CTE$), is the favourable candidate to replace Ablestik in avoiding crack incident in the die edge.

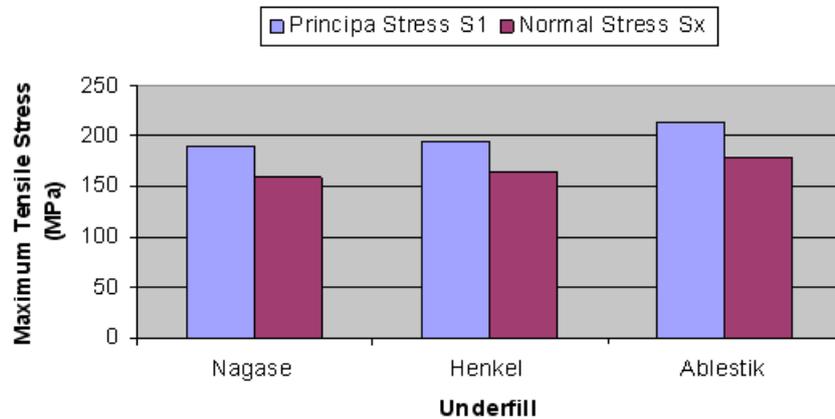


Figure 11 : Maximum tensile stress with different underfill material

4 CONCLUSION

This study has presented a FEA simulation based, comparison in underfill fillet geometry and underfill material properties in investigation the die back edge cracking in FC-CBGA package. The results indicate that:

The die edge crack due the higher tensile stress existed especially in the die corner and edge which interfacing with the top underfill fillet

The die edge crack can be avoided by making the fillet height lower and fillet width/contact angle smaller.

The risk of high tensile stress at die can be reduce by using underfill material which has low multiplication of E modulus and CTE ($E \times CTE$)

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